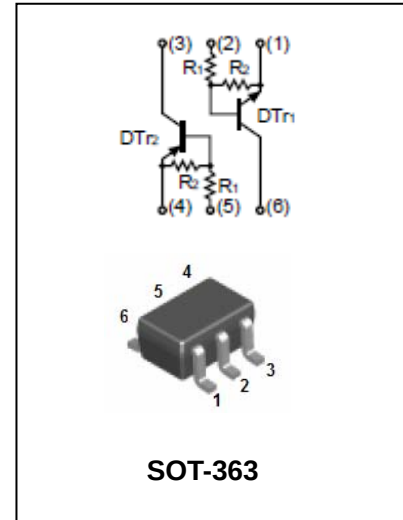


Digital Transistor

UMD9N

FEATURES

- DTA114Y and DTC114Y transistors are built-in SOT-363 package.



APPLICATIONS

- NPN/PNP epitaxial planar silicon transistor.

ORDERING INFORMATION

Type No.	Marking	Package Code
UMD9N	D9	SOT-363

MAXIMUM RATING @ Ta=25°C unless otherwise specified

Symbol	Parameter	Value		Unit
		T _{r1}	T _{r2}	
V _{CC}	Supply Voltage	50	-50	V
V _{IN}	Input Voltage	40	-6	V
I _O	Output Current	70	-70	mA
I _{C(MAX.)}	Collector Current	100	-100	mA
P _c	Power Dissipation (NOTE 1)	150		mW
T _j , T _{stg}	Junction and Storage Temperature	-55 to +150		°C

NOTE 1: 120mW per element must not be exceeded. PNP type negative symbols have been omitted.



Digital Transistor

UMD9N

ELECTRICAL CHARACTERISTICS T_{R1} Section @ Ta=25°C unless otherwise specified

Parameter	Symbol	Test conditions	MIN	TYP	MAX	UNIT
Input Voltage	V _{I(off)}	V _{CC} =5V, I _O =100μA			0.3	V
	V _{I(on)}	V _O =0.3V, I _I =1mA	1.4			V
Output Voltage	V _{O(on)}	I _O =5mA, I _I =0.25mA		0.1	0.3	V
Input Current	I _I	V _I =-5V			0.88	mA
Output Current	I _{O(off)}	V _{CC} =50V, V _I =0V			0.5	mA
DC Current Gain	G _I	V _O =5V, I _O =5mA	68			
Input Resistor	R ₁		7	10	13	kΩ
Resistance Ratio	R ₂ /R ₁		3.7	4.7	5.7	
Gain-Bandwidth Product	f _T	V _{CE} =10V, I _E =5mA, f=100MHz		250		MHz

ELECTRICAL CHARACTERISTICS T_{R2} Section @ Ta=25°C unless otherwise specified

Parameter	Symbol	Test conditions	MIN	TYP	MAX	UNIT
Input Voltage	V _{I(off)}	V _{CC} =-5V, I _O =-100μA			-0.3	V
	V _{I(on)}	V _O =-0.3V, I _I =-1mA	-1.4			V
Output Voltage	V _{O(on)}	I _O =-5mA, I _I =-0.25mA		-0.1	-0.3	V
Input Current	I _I	V _I =-5V			-0.88	mA
Output Current	I _{O(off)}	V _{CC} =-50V, V _I =0V			-0.5	mA
DC Current Gain	G _I	V _O =-5V, I _O =-5mA	68			
Input Resistor	R ₁		7	10	13	kΩ
Resistance Ratio	R ₂ /R ₁		3.7	4.7	5.7	
Gain-Bandwidth Product	f _T	V _{CE} =-10V, I _E =-5mA, f=100MHz		250		MHz

Digital Transistor

UMD9N

TYPICAL CHARACTERISTICS @ $T_a=25^\circ\text{C}$ unless otherwise specified

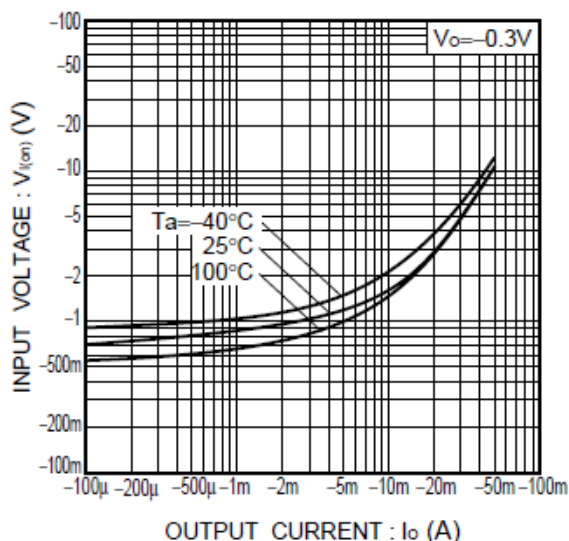


Fig.1 Input voltage vs. output current
(ON characteristics)

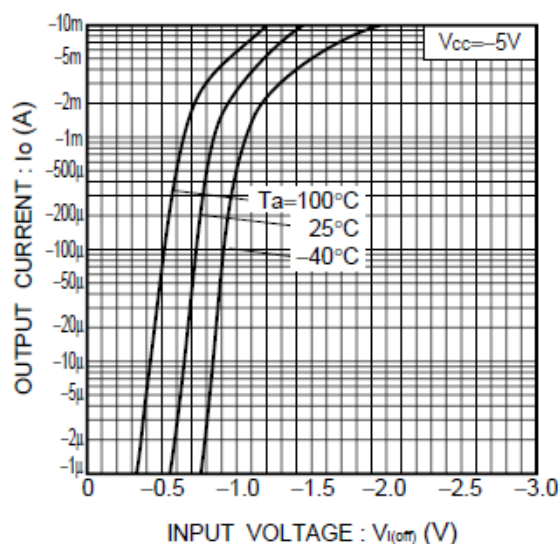


Fig.2 Output current vs. input voltage
(OFF characteristics)

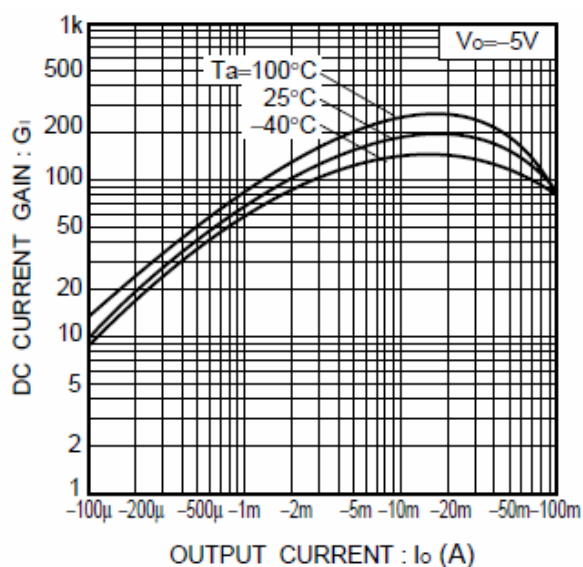


Fig.3 DC current gain vs. output
current

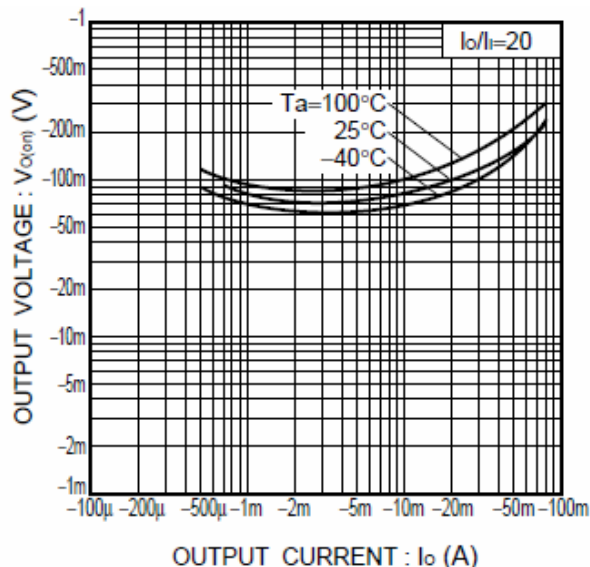


Fig.4 Output voltage vs. output
current

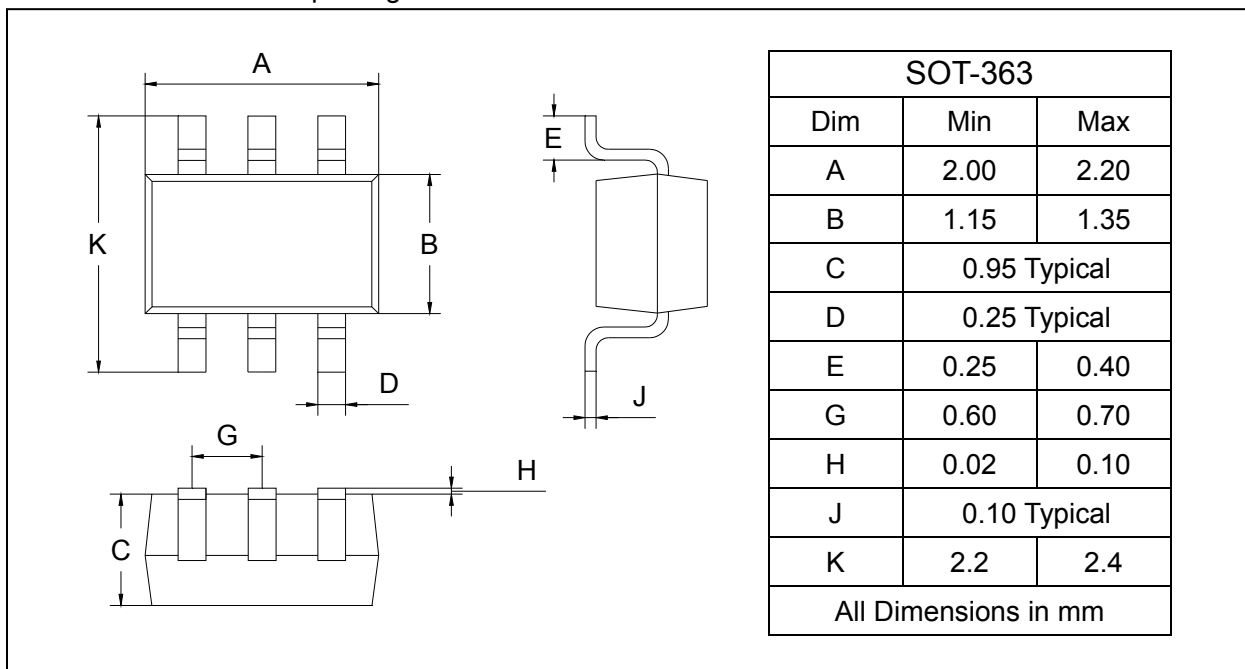
Digital Transistor

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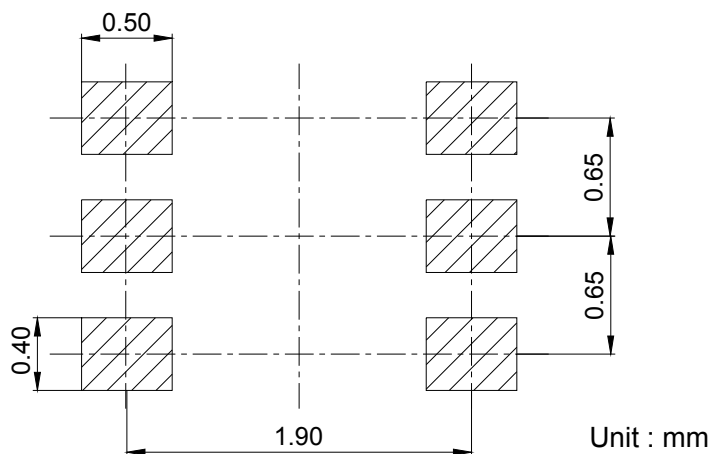
PACKAGE OUTLINE

Plastic surface mounted package

SOT-363



SOLDERING FOOTPRINT



PACKAGE INFORMATION

Device	Package	Shipping
UMD9N	SOT-363	3000/Tape&Reel